

Education:

- Ph.D. in Electrical Engineering, University Of Tehran, Tehran, Iran Sept,2002 - March.2008.
- M.Sc. in Electrical Engineering, Sharif University of Technology, Tehran, Iran, Sept.1997- Jan. 2000.
- B.Sc. in Electrical Engineering, Sharif University of Technology, Tehran, Iran, Sept.1993- Sept.1997.

Thesis:

- Ph.D. Thesis: "Phase Noise Reduction in LC CMOS Oscillators with application to Wireless High-rate OFDM systems".
Advisor: Dr. M. Kamarei, Dr. N. Yazdani, Dept. of Electrical and Computer Eng., University Of Tehran, .
- M.Sc. Thesis: "High-Q High-Frequency Sampled-Data Filter Design in CMOS technology",
Advisor: Dr. M. Sharif-Bakhtiar., Dept. of Electrical Eng., Sharif University of Technology

Journal Publications:

- 1- **Abumoslem Jannesari**, et.al, “On the Parasitic –Sensitivity of Switched-Capacitor Summing-Integrator Structure for Delta-Sigma Modulators”, *IEEE Trans. On Circuit and System-II*, Vol. 50, No.9 Sep. 2003.
- 2- **Abumoslem Jannesari**, Mahmoud Kamarei, "Sinusoidal-Switched Serial-Coupled CMOS LC Quadrature VCO", *IECE Electronics Express*, Vol. 4, No. 13, pp423-429, July 2007.
- 3- **Abumoslem Jannesari**, Mahmoud Kamarei, “Serial-Injection Serial coupled CMOS LC Quadrature VCO”, *IECE Electronics Express*, Vol.4, No.13, July 2007.
- 4- **Abumoslem Jannesari**, Mahmoud Kamarei, “Comments on “A General Theory of Phase Noise in Electrical Oscillators””, *IEEE Journal of Solid-Circuits*, Vol.42, No.10, Oct. 2007.
- 5- **Abumoslem Jannesari**, Mahmoud Kamarei, “Sinusoidal shaping of the ISF in LC Oscillators”, *International Journal of Circuit Theory and Application*, Oct. 2007.
- 6 - Ahamad Hormozai, **Abumoslem Jannesari**, "Multi-Level 2D LUT as Digital Pre-Distorter for linearizing memory affected RF Power Amplifiers", *IEICE Electronics Express*, Vol.8, No.19, Oct. 2011.

- 7- Soroush Moallemi, **Abumoslem Jannesari**, "A High-gain, Wide-band, Fast-Settling Amplifier with Non-Miller capacitor Compensation", IEICE Electronics Express, Vol.8, No.25, Oct. 2011.
- 8- Hedieh Elyasi, **Abumoslem Jannesari**, Abdolreza Nabavi, "A merged LNA and Mixer with Improved noise figure and gain for Software Defined Radio Applications", IEICE Electronics Express, Vol.9, No.3, Feb. 2012.
- 9- Shoeib Rahmatollahi, **Abumoslem Jannesari**, "RFDAC Converter Design Using First Order Hold", Electronics Industries Journal, Quarterly Vol.3, No.1. Spring 2013.
- 10- Hossein HosseiniNejad, **Abumoslem Jannesari**, et al, "Data Compression in Brain-Machine/Computer interfaces Based on the Walsh-Hadamard Transform", IEEE Trans. on Biomedical Circuits and Systems, pp. 128-137, Vol.8, No.1.Feb, 2014.
- 11-Hossein HosseiniNejad, **Abumoslem Jannesari**, et al, "A 128-Channel DCT-Based Neural Signal Processor for Implantable Neural Recording Micro-Systems", International Journal Of Circuit theory and Applications, Wiley,pp489-501, Oct. 2015.
- 12- Alireza Ramez, **Abumoslem Jannesari**, "LDPC Decoder Implementation for WiMAX standard in CMOS technology", Electronics Industries Journal, Vol. 4, No. 4, Winter 2013.
- 13- Aboozar Ghorbani-nejad, **Abumoslem Jannesari**, "A Sub-uW Tuneable Switched-Capacitor Amplifier-Filter for Neural Recording Using a Class-C Inverter", Iranian Journal Of Electrical and Electronics Engineering, Vol.9, No.4, Dec. 2013.
- 14- Tahereh Heidari, **Abumoslem Jannesari**, "Optimum Implementation of a High-Throughput QC-LDPC Decoder Based on Layered Decoding", Accepted to be published on Electronics Industries Journal 2015.
- 15 Samira Jafarzadeh, **Abumoslem Jannesari**, "A High Dynamic Range Digitally-Controlled Oscillator (DCO) for ALL-Digital PLL Systems", Accepted to be published on Modares Journal Of Electrical Engineering 2015.
- 16- Zahra Sohrabi, **Abumoslem Jannesari**, "Complex Conjugate Poles Implementation in discrete-time charge-domain filters", IET Electronics Letters, pp.1236-1238, Vol. 51, Issue. 16, Aug. 2015.
- Conference Presentations:**
- 1- **Abumoslem Jannesari**, S. M. Rezaul Hasan, and, Mehrdad Sharif-Bakhtiar, "Design of Wide-band Op-amp with Improved DC-gain for High-Q High Frequency Switched-Capacitor Filters," Proceedings of the Eleventh Electronics New Zealand Conference, 15-16 November, Palmerston North, New Zealand, 2004, p. 77-81.
- 2- **Abumoslem Jannesari**, et. al, "An efficient way to implement an IEEE 802.11 MAC with embedded systems ", IEEE Conf. ISPACS 2006
- 3-**Abumoslem Jannesari**, Mahmoud Kamarei, "Low phase noise QVCO design with SISC method", ICEE 2007, Tehran, Iran.
- 4- **Abumoslem Jannesari**, Mahmoud Kamarei, "Design of a Low-Voltage low-phase-noise Complementary CMOS VCO", IEEE, ISIC2007, Singapore, Sept. 2007.
- 5- **Abumoslem Jannesari**, Mahmoud Kamarei, "Sine-Switched LC-VCO", ICEE 2008, Tehran, Iran.
- 6- **Abumoslem Jannesari**, et al, "A low power sub-threshold CMOS Continuous-Time Active Filter with reduced in-band noise for WiMax Applications", IEEE APCCAS 2010 Asia Pasific Conf. on Cir. & System, Malaysia 2010.
- 7-Laya Mohahmadi, **Abumoslem Jannesari**, "A Linearly Tunable Ultra Low Power CMOS Transconductance With Its application to Gm-C", IEEE ICEE 2012, Iranian Conf. on Electrical Engineering, Tehran 2012.
- 8- Baback BeigMohahmadi, **Abumoslem Jannesari**, "A Low Power Continuous-Time Low-Power Analog Filter for IEEE 802.11a/b/g/n Wireless LANs", IEEE ICEE 2012, Iranian Conf. on Electrical Engineering, Tehran 2012.

- 9- H. Hosseini-Nejad, [Abumoslem Jannesari](#), Amir M. Sodagar,"Data Compression Based on Discrete-Cosine Transform for Implantable Neural Recording Micro-systems", IEEE, ICCAS2012, Malaysia, Kuala Lumpur, 2012.
- 10- Soroush Moallemi, [Abumoslem Jannesari](#), "The Design of Reconfigurable Delta-Sigma Modulator for Software Defined Radio Applications", IEEE, ICCAS2012, Malaysia, Kuala Lumpur, 2012.
- 11-Soroush Moallemi, [Abumoslem Jannesari](#), "A New Architecture for Two-Stage OTA with No-Miller Capacitor Compensation", IEEE, ICCAS2012, Malaysia, Kuala Lumpur, 2012.
- 12- Shoeib Rahmatollahi, [Abumoslem Jannesari](#), "A Simple Pre-Filtering for Spectral Smoothing in a Radio-Frequency Digital-to-Analog Converter", IEEE, ICCAS2012, Malaysia, Kuala Lumpur, 2012.
- 13-Samira Jafarzadeh, [Abumoslem Jannesari](#), " A Precise Delta-Sigma Based Digitally Controlled Oscillator (DCO) for All-Digital PLL", IEEE ICEE 2013, Iran, Mashhad.
- 14- Tahere Heidar, [Abumoslem Jannesari](#), " Design of High-Throughput QC-LDPC Decoder for WiMAX Standard", IEEE ICEE 2013, Iran , Mashhad.
- 15 - Shoeib Rahmatollahi, Bashir Futouhi, [Abumoslem Jannesari](#), "Dual-Mode High-Gain and High-Q Active RF Bandpass Filter", IEEE ICEE 2013. Iran, Mashhad.
- 16 - Hossein Sahoolizadeh , [Abumoslem Jannesari](#), Massoud Dousti, "A New Pole-Zero Technique for Reducing Thermal Noise to Design a Very Low Noise Figure UWB LNA", IEEE ICEE2014, Tehran, Iran.
- 17 - Mojtaba HasaanNezhad , [Abumoslem Jannesari](#), "High- Frequency Direct Digital Frequency Synthesizer Design with Non-Uniform Sine-Weighted Digital-to-Analog Converter", IEEE IST2014, Tehran, Iran.
- 18 - Mojtaba HasaanNezhad , [Abumoslem Jannesari](#), "High- Frequency Direct Digital Frequency Synthesizer Design with Non-Uniform Squared Sine-Weighted Digital-to-Analog Converter", IEEE IST2014, Tehran, Iran.
- 19 - Baset Mesgari, Saeed Saeedi,, [Abumoslem Jannesari](#), "A Wideband Low Noise Distributed Amplifier With Active Termination", IEEE IST2014, Tehran, Iran.
- 20- Akbar Hemati,, [Abumoslem Jannesari](#), "Harmonic Fold-Back Improvement in the 4-path filter Using Band-pass Charge Domain Sampler", IEEE ICEE 2015, Tehran, Iran.

Books:

- 1- A. Mireshghi, [A. Jannesari](#), "Analog Integrated Circuit Design as Electronics-II concepts in Persian", 1999.
- 2- M. Atarodi, [A. Jannesari](#), et al,"CMOS Analog Integrated Circuit Design", 1999.

References

- Dr. M Kamarei, University Of Tehran, Tehran , Iran
- Dr. M. Sharif-Bakhtiar, Sharif University of Techology,Tehran, Iran
- Dr. S. Ghaemmaghami, Sharif University of Technology, Tehran, Iran.